# MODELING OF THREE PHASE FOUR SWITCH RECTIFIER BY ZERO VOLTAGE SWITCHING

### Sumitranandan Agnihotri<sup>1</sup>, Mrs Manju Khare<sup>2</sup>

<sup>1</sup>M.Tech (Control System) Scholar, Department of EE 'LNCT Bhopal (India) <sup>2</sup>Associate Professor, Department of EE LNCT Bhopal (India)

#### ABSTRACT

A Three-phase four switch boost rectifier is introduce here. A 340 V input is given to the diode rectifier which boost it up to 900V voltage. This is advance technique which is used to get three level of DC voltages for different applications. Here we give 340V input voltage and boost it upto900V output voltage. By using switching we cut voltage into three different level i.e. 450V, 450V and 900V (Overall voltage).

Keywords: Zero Voltage Switching, Boost rectifier, AC and DC Rectifier, Zero Current Switching

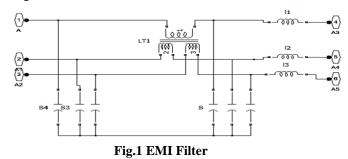
#### I. INTRODUCTION

Previously designed rectifier gives the ideas to design the Simulink model for achieving the high voltage performances but rectification is not easy to do. Here we analyze the model that give the high performances for the same given input.

By using soft switching, we can achieve less than 5% input current total harmonic distortion. These models also automatically balance the output voltage by using the series capacitors and other low cost & high performances components.

#### II. 3-PHASE, 3-LEVEL ZVS RECTIFIER

The 3 Phase I/P lines given to the circuit of rectifier were used by the diodes of the rectifier switches. The Rectifier circuit is controlled by the four MOSFET Switches by controlling pulse width Modulation technique. The Electromagnetic (EMI) filters are also used here that helps to eliminate noise signals which have been superimposed with input current during high voltage transitions and clamping in the circuit. The over all output getcollected in output capacitors and it is clamped with clamped capacitor. The clamped capacitors are connected in series. Here the output voltages with different values are obtained.



448 | Page

#### International Journal of Advanced Technology in Engineering and Science Vol. No.5, Issue No. 05, May 2017 ija

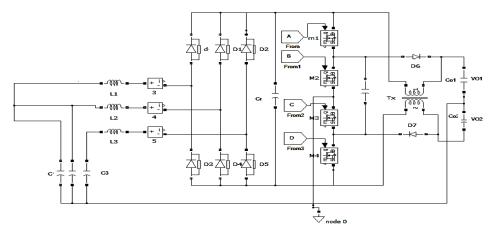


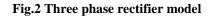
www.ijates.com

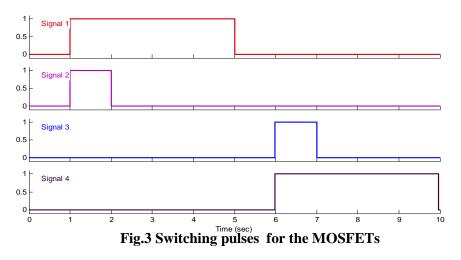
#### **III. MODEL AND ITS SIMULATION**

The three phase rectifier simulink model is shown in figure-2.In this model C1,C2,C3 are the input capacitor connected with star connection which create the neutral point. This connection is also connected with middle of pairs of switches and middle of output capacitors, which help in the balancing the output.

In the figure-3, when the switch S1 and S2 are open the current will flow from the initial Conductors L1 and current Will flow with output voltage of V01 and switches capacitor Vcc will charged. After sometimes witch S2 is disconnected. Vcc will be hence discharged and current flows from S2 and S3.







Thus we assume current I1 is equal to VA/L1.After sometime the switch S1 is also turned off and at this time some delay are occurred at this time zero voltage switching will perform and give the current for the circuit. That's why the circuit is called Zero Voltage Switching Operated Circuit.

After that S3 and S4 switches will beconnected and performances is shown in the figure-4. At this time the current I2 and I3 Flows through inductor L2 and L3 and also current go to V02 side. The inductor current is given by

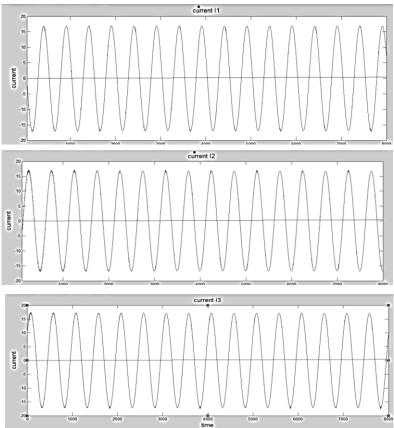
$$i_{L1} = \frac{V_A - (1 - 2D)V_{01}}{2L_1}I_5$$

Here Va is maximum peak of voltage and  $i_{L1}$  indicates the zero voltage switching delay.

### International Journal of Advanced Technology in Engineering and Science Vol. No.5, Issue No. 05, May 2017 www.ijates.com

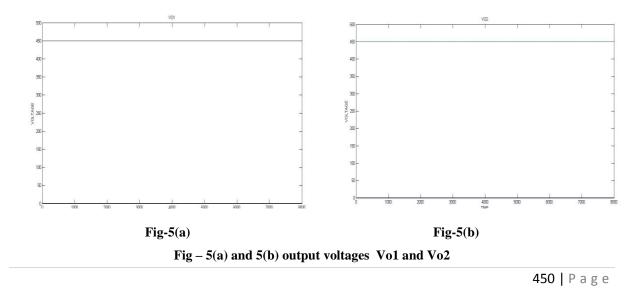
#### **IV. RESULTS**

The results of the matlabsimulink model was evaluated on  $340V_{L-L}$  voltage where the C1,C2 and C3 are  $2.2\mu$ F, L1 , L2 and L3 are  $89\mu$ H.D1-D6 taking the simple ratings and the diodes D7 and D8 having high snubber value.Figure 4shows the input current  $I_1$ ,  $I_2$ , and  $I_3$  waveforms of simulink result in scope at full power at  $340V_{L-L}$  voltage.



#### Fig -4 Input current waveforms

The figure 4 shows the Input current which is measured on the input side .Here we see inputs I1,I2 and I3. The relative THD of these inputs is less than 3% at this current and voltage boost is from 340V line to line to 900 V.



## International Journal of Advanced Technology in Engineering and Science Vol. No.5, Issue No. 05, May 2017

### www.ijates.com



0.	05 0		0.25
			_
			 -
			 -
			 _
			٦

#### Fig –6 overall output voltage

Figure-5 shows two different outputs of 450V each operated by four switches and Figure-6 shows the overall output voltage of 900 V.

#### **V. CONCLUSION**

The total work is done by MATLAB Simulink at 340V Line to Line Voltage. It achieves 94% efficiency at full load.

In this paper Three phase three level high voltage rectifier by zero voltage switching is introduced. The voltage across each switch was clamped by clamping circuit with output voltage, with one half of its measured value. By Simulink model the analyzed result is less than 5% input current THD over the input &above the 8% load.

#### REFERENCES

- [1] Yung taek Jang, Senior Member, IEEE, and Milan M.Jovanovic" A new Three-Level Soft-Switched Converter" IEEE transactions on power electronics,vol.20,no.1, JANUARY 2005
- [2] Y.Jang and M.M. Jovanovic, "The Taipei Rectifier –a new three phase two switch zvspfcdcm boost rectifier ,"IEEE Transaction on Power Electronics, Vol.28,No.2.pp.686-694,feb 2013
- [3] I. Barbi, R. Gules, R. Redl, and N. O. Sokal, "DC/DC converter for high input voltage: four switches with peak voltage of V =2, capacitivemturn-off snubbing, and zero-voltage turn-on," in *Proc. IEEE PowerElectronics Specialists Conf. (PESC)*, 1998, pp. 1–7.
- [4] F. Canales, P. M. Barbosa, J. M. Burdio, and F. C. Lee, "A zero-voltage switching three-level dc/dc converter," in *Proc. IEEE Int. TelecommunicationsEnergy Conf. (INTELEC)*, 2000, pp. 512–517.
- [5] X. Ruan, L. Zhou, and Y. Yan, "Soft-switching PWM three-level converters," *IEEE Trans. Power Electron.*, vol. 16, no. 5, pp. 612–622, Sep.
- [6] J. S. Lai and F. Z. Peng, "Multilevel converters—a new breed of power converters," *IEEE Trans. Ind. Applicat.*, vol. 32, no. 3, pp. 509–517, May/Jun. 1996.
- [7] M.H. Rashid POWER ELECTRONICS "Zero Voltage Switching"

### International Journal of Advanced Technology in Engineering and Science Vol. No.5, Issue No. 05, May 2017 www.ijates.com

- [8] PeterMantovanelli Barbosa "Three-Phase Power Factor Correction Circuits for Low-Cost Distributed Power Systems" Doctor of PhilosophyJuly 31, 2002 Blacksburg, Virginia
- [9] S. J. Jeon, F. Canales, P. M. Barbosa, and F. C. Lee, "A primary-side-assisted zero-voltage and zero-current switching three-level DC-DC converter with phase-shift control," in *Proc. IEEE Applied Power Electronics Conf. (APEC)*, 2002, pp. 641–647.
- [10] P Barbosa ,F.canales and F.C.Lee "Analysis an evaluation of the Two Switch Three Level Boost Rectifier ,"IEEE Power Electronics specialists' conf.(PESE) record ,2001,pp.1159-1164.
- [11] J.W. Kolar, H. Ertl and F.C. Zach, "A Comprehensive Design Approach for a Three-Phase High-Frequency Single-Switch Discontinuous-Mode Boost Power Factor Corrector Based on Analytically Derived Normalized Converter Component Ratings," in IEEE Transactions on Industry Applications, Vol. 31, No. 3, May/June 1995, pp. 569-582. References 225
- [12] J.W. Kolar, H. Ertl and F.C. Zach "Space Vector-Based Analytical Analysis of the Input Current Distortion of a Three-Phase Discontinuous-Mode Boost Rectifier System," in PESC 1993, pp. 696-703.
- [13] Muhdhalalluddin bin abdulrahim "design and simulation of three phase rectifier with power factor correction" Master of Engineering Faculty of Electrical & Electronic Engineering Tun Hussein Onn University of Malaysia JULY 2012