ANALYSIS AND SIMULATION OF Z-SOURCE INVERTER

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ABSTRACT

This paper presents the operating principle and control method of an impedance source inverter known as Zsource inverter (ZSI). This inverter provides a unique feature that cannot be achieved by traditional inverter. ZSI can buck-boost the voltage in a single stage thus reduces the component count, cost and increases the efficiency. The paper analyzed the circuit characteristic and its superiority. Simulation results are also shown to verify the proposed concept and analysis using MATLAB/Simulink.

Keywords: Voltage Source Inverter, Current Source Inverter, Buck-Boost, Pulse Width Modulation (PWM).

I. INTRODUCTION

Power electronic inverters are increasingly being used in modern energy conversion systems, including uninterruptible power supplies, motor drives and active interfaces for localized and distributed generation. Voltage source (VSI) and current source inverters (CSI) are the basic traditional power inverters.

Fig. 1 shows the voltage source inverter. In VSI, a DC voltage source is supported by a large capacitor and a 3 phase inverter is used. Despite of wide applications VSI has some limitations:

- The output of VSI is always less than input DC and act as a buck inverter. For application where high output voltage is required with limited DC, a dc-dc boost converter is used .This additional converter stage lowers the efficiency and increases the cost of the system.
- Another barrier is that the shoot through problem due to the misgating of upper and lower device of same phase leg destroy the device. To avoid this ,dead time is used which causes waveform distortion.





Fig. 2 Current Source Inverter

Fig. 2 shows the current source inverter. It consist of current source in series with an inductor. However the CSI has also some limitations:

- In CSI the input DC voltage is always less than the output voltage and act as a boost inverter .For wide range application an additional dc-DC buck converter is required. This also increases the system cost and reduces efficiency.
- In CSI to avoid the dc inductor to be open circuited at least one of the upper and one of the lower device has to be gated ON simultaneously. An additional overlap time is needed to achieve this target on the cost of waveform distortion.

The conceptual and theoretical barriers of voltage source and current source inverter can be overcome with an impedance source inverter. This paper describes the operating principle, circuit analysis and control method for the Z-source inverter through simulation by MATLAB.

II. Z-SOURCE INVERTER

The new impedance-source power inverter has been recently invented, eliminates all problems of the traditional Voltage source and Current source inverters. The Z-source inverter consists of a unique impedance network which couple the converter main circuit to the power source, load, or other converter, for providing unique features that cannot be observed in the traditional VSI and CSI inverters. The configuration of three phase Z-source inverter is shown in Fig.3. The input power source is a dc voltage source Vdc that is applied to the Z-Source inverter through reverse blocking diode D_{in}. It consists of two inductors L_1 , L_2 and two capacitors C_1 , C_2 connected in cross coupled shape.



Fig. 3 Z Source Inverter

2.1 Operating Principle

The three phase ZSI has nine switching states in spite of VSI that has eight .The additional state is known as shoot through state, which can be generated in seven different ways. The Table I shows all the fifteen switching states of a three phase leg Z source inverter.

Switching States (output voltage)	S1	S4	S3	S6	S 5	S2
Active states {100}(finite)	1	0	0	1	0	1
Active states {110}(finite)	1	0	1	0	0	1
Active states[010}(finite)	0	1	1	0	0	1
Active states {011}(finite)	0	1	1	0	0	1
Active states {001}(finite)	0	1	0	1	1	0
Active states {101}(finite)	1	0	0	1	1	0
Zero states {000} (0V)	0	1	0	1	0	1
Zero states {111} (0V)	1	0	1	0	1	0
Shoot Through states E1(0V)	1	1	1	0	1	0
Shoot Through states E2(0V)	1	0	1	1	1	0
Shoot Through states E3(0V)	1	0	1	0	1	1
Shoot Through states E4(0V)	1	1	1	1	1	0
Shoot Through states E5(0V)	1	1	1	0	1	1
Shoot Through states E6(0V)	1	0	1	1	1	1

Table. ISwitching Sequence of ZSI

• The first six states are known as active state. In this the DC voltage is impressed across the leg. Next two states are null states when the load terminal are shorted through either the upper or lower three devices, respectively.

• Last seven shoot through state can be generated when the load terminals are shorted through both the upper and lower devices of any one phase leg, any two phase leg or all the three phase leg.

This shoot through state proved the buck-boost feature to the Z source inverter

2.2 Circuit Analysis

The circuit analysis of ZSI works on the two modes, non-shoot- through mode (active and null state) and shoot through mode (shoot through state). Assuming that the Z-source network is symmetrical i.e. inductors L_1 and L_2 and capacitor C_1 and C_2 have the same inductance L and capacitance C.

$$v_{L_1} = v_{L_2} = v_L \tag{1}$$

$$v_{C_1} = v_{C_2} = v_C$$
 (2)

2.3.1Nonshoot Through State

The equivalent circuit of the ZSI viewed from the DC link when the inverter bridge is in one of the eight nonshoot-through switching states is shown in Fig. 4.



Fig. 4. Operating State at Mode 1: Non Shoot through

International Journal of Advanced Technology in Engineering and Science www.ijates.com Volume No.03, Issue No. 06, June 2015 SN (online): 2348 – 7550 The inverter bridge is in non-shoot-through state for an interval T₂ during switching cycle T

The inverter bridge is in non-shoot-through state for an interval T₁, during switching cycle T. Applying KVL

$$V_L = V_{dc} - V_c \tag{3}$$

$$V_i = V_{dc} \tag{4}$$

$$v_{PN} = V_C - v_L \tag{5}$$

$$v_{PN} = 2V_C - V_{dc} \tag{6}$$

2.3.2 Shoot Through State

The equivalent circuit of the ZSI viewed from the DC link when the inverter bridge is in shoot-through zero state is shown in Fig. 5.



Fig. 5. Operating State at Mode 2: Shoot Through

In steady state the average voltage of the inductor over one switching cycle should be zero, from (3) and (7)

$$V_{L} = \frac{T_{1}(V_{dc} - V_{C}) + T_{0}(V_{c})}{T}$$
(10)

Or

$$V_{C} = \frac{T_{1}}{T_{1} - T_{0}} \tag{11}$$

Similarly the average DC link voltage across the inverter bridge can be found as follows

$$V_{PN} = \frac{T_1(2V_C - V_{dc}) + T_0(0)}{T}$$
(12)

Or

$$V_{PN} = \frac{T_1}{T_1 - T_0} V_{dc} = V_c$$
(13)

The peak DC link voltage across the inverter bridge is expressed as

$$v_{PN}^{\Lambda} = V_c - v_L = 2V_c - V_{dc} - \frac{T}{T_1 - T_0} V_{dc}$$

$$v_{PN}^{\Lambda} = BV_{dc} \qquad (14)$$

$$T_c$$

Where $B = \frac{T}{T_1 - T_0} = \frac{1}{1 - 2D}$ is the boost factor and $D = \frac{T_0}{T}$

The output peak phase voltage from the inverter can be expressed as

$$v_{ac}^{\Lambda} = M \frac{v_{PN}}{2}$$
(15)

Where M is the modulation index.

On substituting the value of v_{PN}^{Λ} , v_{ac} can be expressed as

$$v_{ac}^{\Lambda} = MB \frac{V_{dc}}{2} \tag{16}$$

or

$$v_{ac}^{\Lambda} = B_B \frac{V_{dc}}{2} \tag{17}$$

Where B_B is the buck-boost factor or the gain G.

The output voltage can be buck-boost by choosing appropriate value of M and B, the value of B can be controlled by the shoot through duty ratio D.

III. PWM CONTROL FOR THE ZSI

How to insert the shoot-through state is the key point of control method for ZSI. For this purpose a modified pulse width modulation control known as Simple Boost Control is used.

In simple boost control the shoot through is inserted with the help of two straight lines, Vp & Vn. Where Vp and Vn is the positive and negative peak value of three phase sinusoidal references When the triangular wave is greater than Vp or lower than Vn the inverter will operate in shoot-through state ,otherwise operate as traditional inverter.Fig 6 shows the simple boost control method to provide the shoot through in Z Source inverter.



Fig. 6. Simple Boost control

The relation between shoot through duty ratio and modulation index is expressed as:

$$D = \frac{T_0}{T} = (1 - M) \tag{18}$$

From (16) & (17) we obtain the boost factor and voltage gain in terms of modulation index represented as

$$B = \frac{1}{1 - 2D} = \frac{1}{2M - 1} \tag{19}$$

$$G = MB = \frac{M}{2M - 1} \tag{20}$$

IV. SIMULATION RESULTS

To verify the validity of analysis a simulation model is designed. In the simulation all the components are ideal. Table II provides a list of simulation parameter for ZSI.

Input DC voltage (V _{dc})	100 V
Inductors $(L_1 = L_2)$	1mH
Capacitors $(C_1=C_2)$	1000µF
Switching frequency (f_s)	10kHz
Filter inductor (L _f)	1mH
Filter Capacitors (C _f)	20 µF
Three phase resistive load	10Ω /phase

Table Ii Simulation Parameters of The Zsi

To produce the output line to line voltage of 208 V_{rms} from the 100 V input DC voltage with simple boost control for the ZSI, according to (20), *M*=0.5865.Thus from the above analysis we obtain

$$D = (1 - M) = 0.4135$$

$$B = \frac{1}{1 - 2D} = 5.78$$

$$G = MB = 3.39$$
$$V_{c} = V_{PN} = \frac{T_{1}}{T_{1} - T_{0}} V_{dc} = 337.28V$$
$$v_{ac}^{\Lambda} = M \frac{v_{PN}}{2} = 169.5V$$

Fig. 7 shows the simulation results of simple boost control techniques for ZSI. It follows the theoretical concepts and provide the required shoot through state to the inverter.





Fig. 7. Simulation Result of Simple Boost Control

Fig.8 shows the output voltage of traditional voltage source inverter under the same input voltage 100V and modulation index of 0.5865. Thus from the calculation



Fig. 8. Simulation Result of the Voltage Source Inverter Using the Traditional PWM Control

Fig.(9) shows the capacitor voltage which is boosted to 337.28V by using simple boost control method .As Shown in fig.(10), it is clear that with the help of Z Source inverter we can achieve a peak phase voltage of 169.5 V, which implies that the line to line voltage is 208 Vrms or 294V peak at the same input voltage. This can overcome the voltage limitation of the traditional inverter. Therefore the above therotical values are quite consistent with the simulation result.



Fig. 9. Simulation Result of the Dc Link Voltage of Z Source Inverter with Simple Boost Control



Fig. 10. Simulation Result of the Output Voltage of Z Source Inverter with Simple Boost Control

V.CONCLUSION

This paper has presented an impedance source inverter that can produce an output voltage greater than the input by controlling the boost factor. This feature is not possible with the traditional inverters. To achieve this, a modified PWM control method is employed with inverter system. The ZSI can be widely used in photovoltaic application where a low input dc voltage is inverted to high output ac voltage.

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